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NOTICE OF ALLOWANCE AND FEE(S) DUE

26574	7590	06/23/2009

SCHIFF HARDIN, LLP
PATENT DEPARTMENT
6600 SEARS TOWER
CHICAGO, IL 60606-6473

EXAMINER
KUCAB, JAMIE R

ART UNIT	PAPER NUMBER
3621	

DATE MAILED: 06/23/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/690,012	10/21/2003	Gerrit Bleumer	P03,0338	4823

TITLE OF INVENTION: METHOD AND ARRANGEMENT FOR VARIABLY GENERATING CRYPTOGRAPHIC SECURITIES IN A HOST DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	$1510	$300	$0	$1810	09/23/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

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If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.

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Complete and send this form, together with applicable fee(s), to: <u>Mail</u>

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INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

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_____ (Depositor's name)

_____ (Signature)

_____ (Date)

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nonprovisional	NO	$1510	$300	$0	$1810	09/23/2009

EXAMINER	ART UNIT	CLASS-SUBCLASS
KUCAB, JAMIE R	3621	705-050000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

❑ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

❑ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list

(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,

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1_____

2_____

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE   (B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent) : ❑ Individual ❑ Corporation or other private group entity ❑ Government

4a. The following fee(s) are submitted:

❑ Issue Fee

❑ Publication Fee (No small entity discount permitted)

❑ Advance Order - # of Copies _____

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❑ A check is enclosed.

❑ Payment by credit card. Form PTO-2038 is attached.

❑ The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. **Change in Entity Status** (from status indicated above)

❑ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

❑ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

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This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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KUCAB, JAMIE R

ART UNIT	PAPER NUMBER
3621	

DATE MAILED: 06/23/2009

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)
(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 991 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 991 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.

	Application No.	Applicant(s)
Notice of Allowability	10/690,012	BLEUMER, GERRIT
	Examiner	Art Unit	
	JAMIE KUCAB	3621	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *amendment and remarks filed 3/23/09*.

2. ☒ The allowed claim(s) is/are *20 and 23-25*.

3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a) ☒ All   b) ☐ Some*   c) ☐ None   of the:

        1. ☒ Certified copies of the priority documents have been received.

        2. ☐ Certified copies of the priority documents have been received in Application No. _____ .

        3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

    * Certified copies not received: _____ .

  Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. **THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. ☒ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

    (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached

        1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____ .

    (b) ☒ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date *20090612*.

    Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)

2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)

3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date _____

4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

5. ☐ Notice of Informal Patent Application

6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____ .

7. ☒ Examiner's Amendment/Comment

8. ☐ Examiner's Statement of Reasons for Allowance

9. ☐ Other _____ .

/Jalatee Worjloh/
Primary Examiner, Art Unit 3685

EXAMINER'S AMENDMENT

Acknowledgements

1.　　　Applicants' amendment filed March 23, 2009 is acknowledged.

2.　　　This paper is assigned Paper No. 20090612 by the Examiner

Examiner's Amendment

3.　　　An examiner's amendment to the record appears below. Should the changes and/or

additions be unacceptable to Applicant, an amendment may be filed as provided by 37 CFR

1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the

payment of the issue fee.

4.　　　Authorization for this examiner's amendment was given in a telephone interview with

Steven Noll (USPTO Registration No. 28,982) on June 10, 2009. The text of claims 20 and 23-

25 as amended below were also authorized via email by Mr. Noll on June 15, 2009.

5.　　　The application has been amended as follows:

　A)　　Cancel non-allowable claims 1, 21, 22, and 26.

　B)　　Amend claims 20 and 23-25 (renumbered claims 1-4) as follows:

　　　　　　20.　　~~The method as claimed in claim 1 further comprising:~~ A computed-

　　　　implemented method for variably generating cryptographic securities for

　　　　communications, involving a host device, comprising the steps of:

　　　　　　cryptographically securing a communication for a first purpose using a

　　　　first signature;

cryptographically securing a communication for a second purpose using a

second signature;

in a processor, using a cryptographic algorithm of a first type to generate

said first signature;

using a cryptographic algorithm of a second type in said processor to

generate said second signature, said cryptographic algorithms of said

first type and said second type, for a same input set, respectively

generating different respective outputs from said processor;

entering an input set into said processor for a current communication

together with an entry designating whether said current

communication is for said first purpose or for said second purpose;

when said current communication is designated for said first purpose,

operating on said input set in said processor for said current

communication with said cryptographic algorithm of said first type to

secure said current communication with said first signature and

emitting said current communication secured with said first signature

as a secured communication output from said processor;

when said current communication is designated for said second purpose,

operating on the same input set in said processor for said current communication

with said cryptographic algorithm of said second type to secure said current

communication with said second signature and emitting said current

communication secured with said second signature as a secured communication

output from said processor;

in a memory of a postal security device, storing a first program that, when

executed, implements said cryptographic algorithm of said first type and

providing storing a second program that, when executed, implements said

cryptographic algorithm of said second type;

employing a hardware unit, outside of and in communication with said

postal security device, as said processor;

when said current communication is designated for said first purpose,

accessing said memory of said postal security device, from said hardware unit and

executing said first program in said hardware unit to secure said communication

for said first purpose with a first signature produced by said cryptographic

algorithm of said first type; and

when said current communication is designated for said second purpose,

accessing said second program said memory of said postal security device from

said hardware unit and, in said hardware unit, executing said second program to

secure said second communication for said second purpose with a signature

generated by said cryptographic algorithm of said second type.

23.   ~~The method as claimed in claim 21 further comprising:~~ A computed-

implemented method for variably generating cryptographic securities for

communications, involving a host device, comprising the steps of:

cryptographically securing a communication for a first purpose using a

first signature;

cryptographically securing a communication for a second purpose using a

second signature;

in a processor, using a cryptographic algorithm of a first type to generate

said first signature;

using a cryptographic algorithm of a second type in said processor to

generate said second signature, said cryptographic algorithms of said

first type and said second type, for a same input set, respectively

generating different respective outputs from said processor;

entering an input set into said processor for a current communication

together with an entry designating whether said current

communication is for said first purpose or for said second purpose;

when said current communication is designated for said first purpose,

operating on said input set in said processor for said current

communication with said cryptographic algorithm of said first type to

secure said current communication with said first signature and

emitting said current communication secured with said first signature

as a secured communication output from said processor;

when said current communication is designated for said second purpose,

operating on the same input set in said processor for said current communication

with said cryptographic algorithm of said second type to secure said current

communication with said second signature and emitting said current

communication secured with said second signature as a secured communication

output from said processor;

generating said first signature exclusively in a first logic circuit module

that executes said cryptographic algorithm of said first type therein under control

of a first implementation program; and

generating said second signature exclusively in a second logic circuit

module by executing said cryptographic algorithm of said second type therein

under control of a second implementation program;

storing said first and second implementation programs in a postal security

device accessible by each of said first and second logic circuit modules;

accessing said first implementation program in said postal security device

from said first logic circuit module when said current communication is

designated for said first purpose; and

accessing said implementation program in said postal security device from

said second logic circuit module when said current communication is designated

for said second purpose.

24.   ~~The method as claimed in claim 21 wherein said host device contains a~~

~~postal security device, and further comprising:~~ A computed-implemented method

for variably generating cryptographic securities for communications, involving a

host device containing a postal security device, comprising the steps of:

cryptographically securing a communication for a first purpose using a

first signature;

cryptographically securing a communication for a second purpose using a

second signature;

in a processor, using a cryptographic algorithm of a first type to generate

said first signature;

using a cryptographic algorithm of a second type in said processor to

generate said second signature, said cryptographic algorithms of said

first type and said second type, for a same input set, respectively

generating different respective outputs from said processor;

entering an input set into said processor for a current communication

together with an entry designating whether said current

communication is for said first purpose or for said second purpose;

when said current communication is designated for said first purpose,

operating on said input set in said processor for said current

communication with said cryptographic algorithm of said first type to

secure said current communication with said first signature and

emitting said current communication secured with said first signature

as a secured communication output from said processor;

when said current communication is designated for said second purpose,

operating on the same input set in said processor for said current communication

with said cryptographic algorithm of said second type to secure said current

communication with said second signature and emitting said current

communication secured with said second signature as a secured communication

output from said processor;

generating said first signature exclusively in a first logic circuit module

that executes said cryptographic algorithm of said first type therein under control

of a first implementation program; and

generating said second signature exclusively in a second logic circuit

module by executing said cryptographic algorithm of said second type therein

under control of a second implementation program;

storing said first implementation program in a memory of said host device

outside of said postal security device; and

storing said second implementation program in said memory of said host

device outside of said postal security device;

accessing said first implementation program in said memory from said

first logic circuit module when said current communication is for said

first purpose; and

accessing said second implementation program in said memory from said

second logic circuit module when said current communication is for said second

purpose.

25.   ~~The method as claimed in claim 1 further comprising:~~ A computed-

implemented method for variably generating cryptographic securities for

communications, involving a host device, comprising the steps of:

cryptographically securing a communication for a first purpose using a

first signature;

cryptographically securing a communication for a second purpose using a

second signature;

in a processor, using a cryptographic algorithm of a first type to generate

said first signature;

using a cryptographic algorithm of a second type in said processor to

generate said second signature, said cryptographic algorithms of said

first type and said second type, for a same input set, respectively

generating different respective outputs from said processor;

entering an input set into said processor for a current communication

together with an entry designating whether said current

communication is for said first purpose or for said second purpose;

when said current communication is designated for said first purpose,

operating on said input set in said processor for said current

communication with said cryptographic algorithm of said first type to

secure said current communication with said first signature and

emitting said current communication secured with said first signature

as a secured communication output from said processor;

when said current communication is designated for said second purpose,

operating on the same input set in said processor for said current communication

with said cryptographic algorithm of said second type to secure said current

communication with said second signature and emitting said current

communication secured with said second signature as a secured communication

output from said processor;

storing a plurality of algorithms selected from the group consisting of

signing algorithms and hash algorithms in a read-only memory of a postal security

device;

from a logic circuit module outside of said postal security device having

access to said memory, accessing a selected one of said algorithms when said

current communication is designated for said first

purpose and using said selected one of said algorithms as said

cryptographic algorithm of said first type in said logic circuit module to

secure said communication for said first purpose; and

from said logic circuit module, accessing a selected different one of said

algorithms from said read-only memory of said postal security device

and, when said current communication is designated for said

second purpose, securing said communication for said second purpose

in said logic circuit module using said selected different one of said

algorithm as said cryptographic algorithm of said second type.

6.   Any comments considered necessary by Applicant must be submitted no later than the

payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for

Allowance."

Drawings

7.   New corrected drawings in compliance with 37 CFR 1.121(d) are required in this application because at least figure 3 includes a handwritten label (e.g. cryptoalgorithm 2). Applicant is advised to employ the services of a competent patent draftsperson outside the Office, as the U.S. Patent and Trademark Office no longer prepares new drawings. The corrected drawings are required in reply to the Office action to avoid abandonment of the application. The requirement for corrected drawings will not be held in abeyance.

Conclusion

8.   References considered pertinent to Applicant's disclosure are listed on form PTO-892. All references listed on form PTO-892 are cited in their entirety.

9.   Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jamie Kucab whose telephone number is 571-270-3025. The examiner can normally be reached on Monday-Friday 9:30am-6:00pm EST.

10.   If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Andrew Fischer can be reached on 571-272-6779. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

11.   Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would

like assistance from a USPTO Customer Service Representative or access to the automated

information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

JK

/Jalatee Worjloh/
Primary Examiner, Art Unit 3685